

BOOTING FROM A RE-PROGRAMMABLE MEMORY
ON AN UNCONFIGURED BUS

Abstract of the Disclosure

Boot up instructions may be stored on a memory coupled to the peripheral component interconnect (PCI) bus. These instructions may be accessed, despite the fact that

5 peripheral component interconnect devices are normally not active during the boot up sequence. As a result, both the basic input/output system and other information may be stored on a reprogrammable memory coupled to the PCI bus.

In some embodiments, this may reduce costs by avoiding the

10 need for two semiconductor memories, one on the PCI bus and the other on a legacy bus.